

FIG. 4

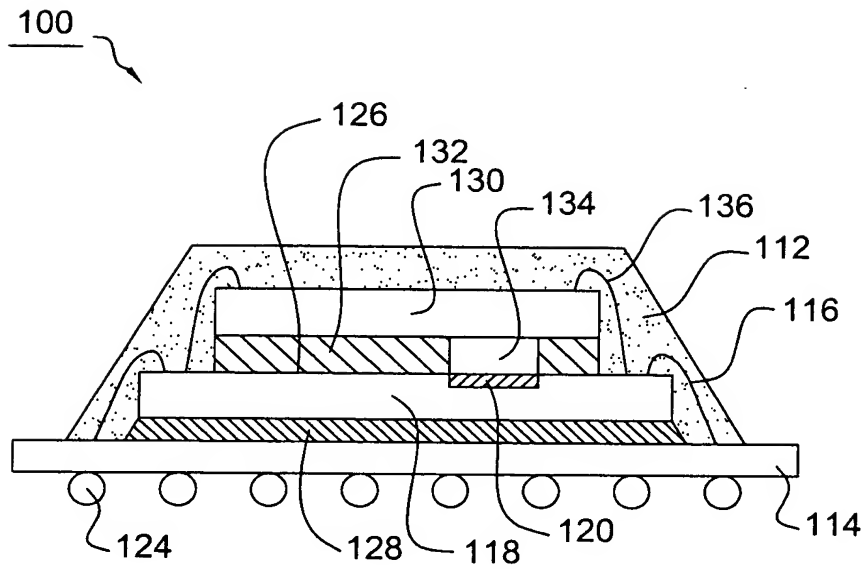


FIG. 2

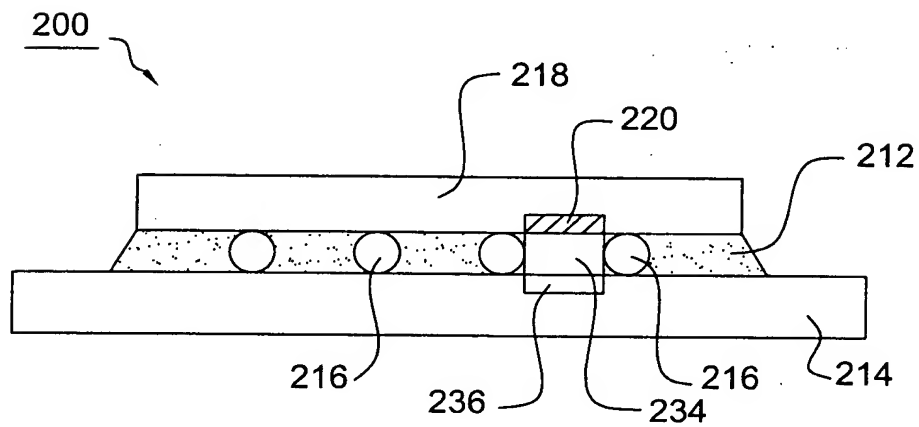


FIG. 3

SEMICONDUCTOR PACKAGE

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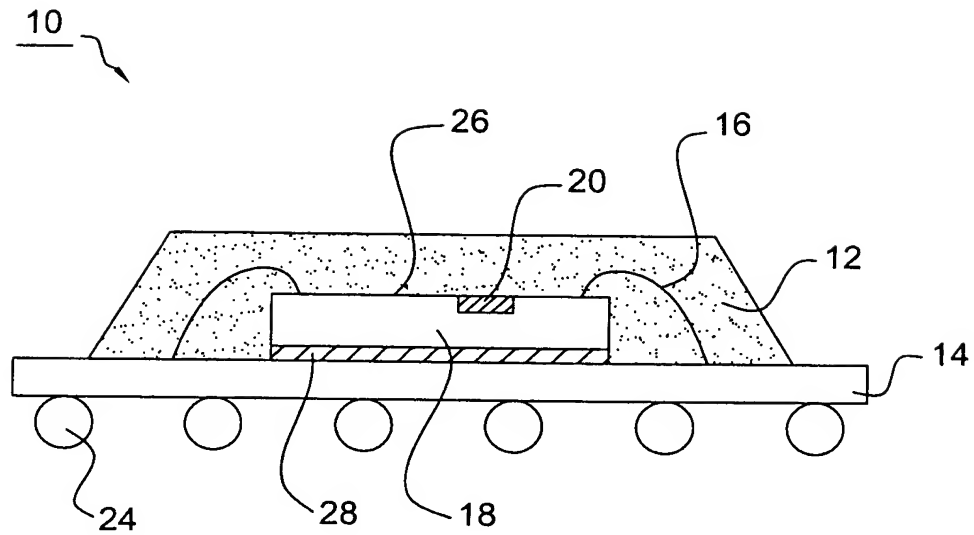


FIG. 1 (PRIOR ART)